

IN THE CLAIMS:

Please cancel claims 11, 12, 15, 16 and 23.

Please amend the claims as shown in the following claims listing.

1. (Currently amended) A method for dynamically reconfiguring a computing system, the method comprising:
detecting a predetermined condition triggering a reconfiguration of the computing system; ~~and~~
dynamically reconfiguring a signal path affected by the condition from a first mode to a second mode responsive to detecting the condition;
operating the affected signal path in the first mode prior to reconfiguration by separating a plurality of information in a transaction into two messages and transmitting the two messages in parallel, each on a respective half of the affected signal path; and
operating the affected signal path in the second mode subsequent to the reconfiguration by transmitting the two messages in series on a single half of the affected signal path.
2. (Original) The method of claim 1, wherein detecting the predetermined condition includes one of: detecting a failure; detecting an opportunity to repair a previously detected failure, and detecting an opportunity to take a system domain affected by the condition off-line so that other system domains do not have to be reconfigured.
3. (Original) The method of claim 2, wherein the computing system includes at least one system control board and wherein detecting the failure includes detecting the failure from the system control board.
4. (Original) The method of claim 1, wherein the computing system includes a

plurality of system domains and detecting the predetermined condition includes detecting the predetermined condition from one of the system domains.

5. (Original) The method of claim 4, wherein the computing system includes at least one system control board and the method further comprises notifying the system control board of the error from an affected system domain.

6. (Original) The method of claim 2, wherein detecting the failure includes detecting the failure during normal operations.

7. (Original) The method of claim 1, wherein dynamically reconfiguring the signal path includes: configuring an I/O switch defining a first end of the affected signal path from the first to the second mode; and configuring a crossbar switch electrically defining a second end of the affected signal path from the first mode to the second mode.

8. (Original) The method of claim 1, further comprising defining a plurality of system domains between which the affected signal path runs.

9. (Original) The method of claim 8, wherein configuring the affected system domains includes: configuring a first switch in a first affected domain defining a first end of the affected signal path from the first to the second mode; and configuring a crossbar switch defining a second end of the affected signal path from the first mode to the second mode.

10. (Original) The method of claim 8, wherein the computing system includes a system control board and configuring the affected system domains includes configuring the system domains from the system control board.

11-12. (Cancelled)

13. (Original) The method of claim 1, wherein dynamically reconfiguring the signal

path includes: disabling the affected signal path; reconfiguring the hardware elements of the disabled signal path from the first mode to the second mode; and re-enabling the signal path.

14. (Original) The method of claim 13, wherein reconfiguring the hardware elements of the signal path includes: configuring a first switch defining a first end of the affected signal path from the first to the second mode; configuring a crossbar switch defining a second end of the affected signal path from the first mode to the second mode.

15-16. (Cancelled)

17. (Currently amended) A computing system, comprising:
a plurality of I/O switches;
a crossbar switch;
a plurality of signal paths, each signal path being defined by one of the I/O switches and the crossbar switch; and
a system controller capable of detecting a condition triggering a reconfiguration and dynamically reconfiguring at least one of the signal paths affected by the condition from a first mode to a second mode;
wherein the first mode includes separating a plurality of information in each transaction into two messages and transmitting the two messages in parallel; and
wherein the second mode includes transmitting the two messages in series on a single half of the signal path.

18. (Original) The computing system of claim 17, wherein the system controller is capable of detecting one of:
detecting a failure;
detecting an opportunity to repair a previously detected failure, and detecting an opportunity to take a system domain affected by the condition off-line so that other system domains do not have to be reconfigured.

19. (Original) The computing system of claim 18, wherein the computing system includes at least one system control board.
20. (Original) The computing system of claim 18, wherein detecting the failure includes detecting the failure during normal operations.
21. (Original) The computing system of claim 17, wherein dynamically reconfiguring the signal path includes: configuring the I/O switch from the first to the second mode; configuring the crossbar switch from the first mode to the second mode.
22. (Original) The computing system of claim 17, further comprising a plurality of system domains between which the affected signal path runs.
23. (Cancelled)
24. (Original) The computing system of claim 17, wherein dynamically reconfiguring the signal path includes: disabling the affected signal path; reconfiguring the hardware elements of the disabled signal path from the first mode to the second mode; re-enabling the signal path; and repeating the previous three steps if a deadlock occurs.
25. (Original) The computing system of claim 24, wherein reconfiguring the hardware elements of the signal path includes: configuring a first switch defining a first end of the signal path from the first to the second mode; and configuring a crossbar switch defining a second end of the signal path from the first mode to the second mode.
26. (Currently amended) The computing system of claim 17, wherein dynamically reconfiguring a signal path affected by the condition from a first mode to a second mode includes dynamically reconfiguring the signal path affected by the condition from a normal mode to a degraded mode.

27. (Currently amended) The computing system of claim 17, wherein dynamically reconfiguring a signal path affected by the condition from a first mode to a second mode includes dynamically reconfiguring the signal path affected by the condition from a degraded mode to a normal mode.